

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

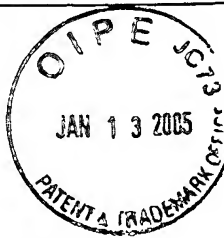
Applicant(s): TSURUTA

Application No.: 10/699,831

Filed: November 4, 2003

Title: SEMICONDUCTOR SUBSTRATE
AND MANUFACTURING PROCESS
THEREFOR

Attorney Docket No.: 11-202



Group Art Unit: 2815

Examiner: WARREN, Matthew E.

Commissioner for Patents
Arlington VA 22202

January 12, 2005

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to a Restriction Requirement mailed on December 10, 2004 in connection with the above application, Applicant hereby elects Invention I (claims 1-5, drawn to a semiconductor device) without traverse.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "D. G. Posz".

David G. Posz
Reg. No. 37,701

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